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**\*BIBDATASHEET\***

CONFIRMATION NO. 7159

Bib Data Sheet

SERIAL NUMBER 10/685,571	FILING DATE 10/16/2003  RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. 8070-1003
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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
*None WLL*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2002-303631 10/17/2002 *WLL*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 01/21/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>WLL</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 29	INDEPENDENT CLAIMS 7
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Verified and Acknowledged  
 Examiner's Signature \_\_\_\_\_ Initials \_\_\_\_\_

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TITLE  
 Method for manufacturing semiconductor device and semiconductor device

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		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )